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Q102  
NEC01P075-AMB  
PATENTS  
2/5/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Eiji HASEGAWA

Confirmation No. 1068

Serial No. 09/883,373

Group 2811

Filed June 19, 2001

Examiner Unassigned

SEMICONDUCTOR DEVICE HAVING A PLURALITY OF GATE INSULATING FILMS OF DIFFERENT THICKNESSES, AND METHOD OF MANUFACTURING SUCH SEMICONDUCTOR DEVICE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents

Washington, D.C. 20231

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the accompanying documents, copies of which are attached to this statement, are made of record on the enclosed sheet.

A concise explanation of the relevance of this item is that this reference was cited by the Taiwanese Patent Office in the corresponding Taiwanese application. A copy of the Taiwanese Official Action in which it was cited is attached hereto, with what is believed to be the pertinent portion enclosed in a wavy line. An English translation of the enclosed portion is also attached hereto.

Under the provisions of 37 CFR 1.97(e), the undersigned hereby certifies that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign Patent Office in a counterpart

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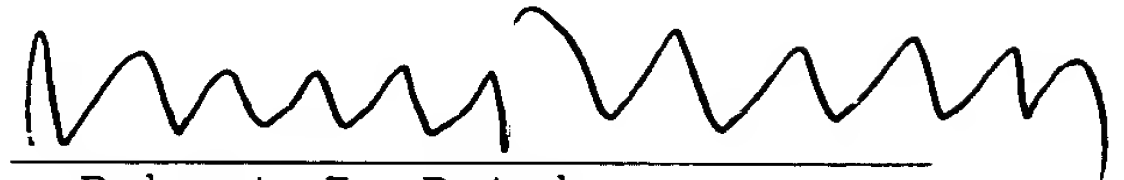
S.N. 09/883,373

foreign application not more than three months prior to the  
filing of this Statement.

Respectfully submitted,

YOUNG & THOMPSON

By

A handwritten signature in black ink, appearing to read 'Robert J. Patch', written over a horizontal line.

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June 3, 2002